

### Date Sheet for 1<sup>st</sup> Sessional (1<sup>st</sup> year)

Date	VLSI Design		ES	
	9.30-10.30 am	2.30-3.30 pm	9.30-10.30 am	2.30-3.30 pm
28/9/2015	VHDL	VDC	DCN	ST
29/9/2015	VT	DAMM	RTS	DPD
30/9/2015	SDP		DSD	

### Date Sheet for 2<sup>nd</sup> Sessional (1<sup>st</sup> year)

Date	VLSI Design		ES	
	9.30-10.30 am	2.30-3.30 pm	9.30-10.30 am	2.30-3.30 pm
28/10/2015	VDC	VHDL	ST	DCN
29/10/2015	DAMM	VT	DPD	RTS
30/10/2015	SDP		DSD	

### Date Sheet for 3<sup>rd</sup> Sessional (1<sup>st</sup> year)

Date	VLSI Design		ES	
	9.30-10.30 am	2.30-3.30 pm	9.30-10.30 am	2.30-3.30 pm
18/11/2015	SDP	VT	DSD	RTS
19/11/2015	VDC	VHDL	ST	DCN
20/11/2015	DAMM		DPD	

### Date Sheet for 1<sup>st</sup> Sessional (2<sup>nd</sup> year)

	VLSI Design	EPDT	ES
Date	11.00 am-12.00 pm	11.00 am-12.00 pm	11.00 am-12.00 pm
28/9/2015			
29/9/2015	MEMS	MEMS	EI
30/9/2015	SOC	EI	SOC

### Date Sheet for 2<sup>nd</sup> Sessional (2<sup>nd</sup> year)

	VLSI Design	EPDT	ES
Date	11.00 am-12.00 pm	11.00 am-12.00 pm	11.00 am-12.00 pm
28/10/2015		EI	EI
29/10/2015	MEMS	MEMS	
30/10/2015	SOC		SOC

### Synopsis Presentation (2<sup>nd</sup> year)

Date	09:30 am to 1 :00 pm
28/10/2015	VLSI Design
29/10/2015	ES
30/10/2015	EPDT

### Date Sheet for 3<sup>rd</sup> Sessional (2<sup>nd</sup> year)

	VLSI Design	EPDT	ES
Date	11.00 am-12.00 pm	11.00 am-12.00 pm	11.00 am-12.00 pm
18/11/2015		EI	EI
19/11/2015	MEMS	MEMS	
20/11/2015	SOC		SOC

### Mid-term Thesis Internal Presentation (2<sup>nd</sup> year)

Date	09:30 am to 1 :00 pm
18/11/2015	VLSI Design
19/11/2015	ES
20/11/2015	EPDT